

	Document ID	Issue Date	Pages	Title	Current OR	Current XRef
1	US 6535567 B1	20030318	10	Method and apparatus for suppression of jitter in data transmission systems	375/372	370/516; 375/373
2	US 6489852 B1	20021203	10	Slew controlled frame aligner for a phase locked loop	331/25	327/156; 331/1A; 331/16; 375/376
3	US 6449315 B2	20020910	13	Serial line synchronization method and apparatus	375/282	375/333; 375/361
4	US 6178216 B1	20010123	21	Digital phase locked loop circuit and method therefor	375/376	327/147
5	US 6118836 A	20000912	14	Frequency and phase locking apparatus	375/376	327/147; 327/156; 331/11
6	US 6088414 A	20000711	14	Method of frequency and phase locking in a plurality of temporal frames	375/376	375/373
7	US 6002709 A	19991214	37	Verification of PN synchronization in a direct-sequence spread-spectrum digital communications system	375/150	370/350; 370/503; 370/514; 370/515; 370/516; 375/359; 375/362; 375/365; 375/367; 375/371; 375/373
8	US 5969631 A	19991019	30	Method and control system for the synchronized transmission of digital data	340/825.21	340/3.21; 370/503; 370/509; 370/514; 370/516; 370/520; 375/362; 375/364; 375/371; 375/373; 375/376
9	US 5872821 A	19990216	11	Arrangement for generating digital signals	375/371	375/372; 375/373
10	US 5790615 A	19980804	12	Digital phase-lock loop network	375/376	370/208
11	US 5752176 A	19980512	15	FM radio receiver and signal processing device used therein	455/184.1	375/376; 455/186.1

	Inventor	Image Doc. Displayed
1	Girardeau, Jr., James Ward	US 6535567
2	Beaulieu, Rejean	US 6489852
3	Richards, Glenn L.	US 6449315
4	Lee, Ig-yong	US 6178216
5	Dove, Jason W. et al.	US 6118836
6	Dove, Jason W. et al.	US 6088414
7	Hendrickson, Alan F.	US 6002709
8	Ammner, Manfred et al.	US 5969631
9	Schoffel, Peter	US 5872821
10	Beale, Terrance Ralph et al.	US 5790615
11	Kimura, Kazuhiro et al.	US 5752176

	Document ID	Issue Date	Pages	Title	Current OR	Current XRef
12	US 5267267 A	19931130	29	Timing extraction method and communication system	375/294	370/503; 375/360; 375/368; 375/371
13	US 4920546 A	19900424	43	Frame synchronizing apparatus	375/354	340/825.2 , 370/509; 375/373
14	US 4682327 A	19870721	26	Polyphase phase lock oscillator	370/503	370/516; 375/362; 375/376
15	US 4453260 A	19840605	13	Synchronizing circuit for detecting and interpolating sync signals contained in digital signal	375/362	370/509; 375/357; 375/373
16	US 4095226 A	19780613	15	System for communication	342/393	375/367; 375/376
17	US 4066964 A	19780103	43	Communication system	375/260	340/7.42; 340/7.46; 375/279; 375/373; 380/34; 455/502; 455/65
18	US 4048563 A	19770913	13	Carrier-modulated coherency monitoring system	375/367	375/371; 375/373; 455/13.2
19	US 3908084 A	19750923	12	High frequency character receiver	375/373	370/517; 375/369
20	US 3699261 A	19721017	6	FRAME SYNCHRONIZING CIRCUIT FOR HIGH CLOCK FREQUENCY DIGITAL COMMUNICATION	370/509	370/514; 370/517; 375/368; 375/373
21	US 3558824 A	19710126	8	TIME-DIVISIONAL PHASE SYNCHRONIZING APPARATUS FOR A TIME-DIVISIONAL MULTIPLE SIGNAL OF BURST MODE	370/517	370/507; 375/376

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12	Kazawa, Tohru et al.	US 5267267
13	Iguchi, Kazuo et al.	US 4920546
14	Okumura, Yasuyuki et al.	US 4682327
15	Inagawa, Jun et al.	US 4453260
16	Kratzer, Dale L.	US 4095226
17	Costanza, Samuel T. et al.	US 4066964
18	Osborne, Eugene F.	US 4048563
19	Wiley, Paul Ronald	US 3908084
20	Tomozawa, Atsushi	US 3699261
21	Ogawa, Akira et al.	US 3558824